



09/22/00

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

1.

U.S. **UTILITY** Patent Application

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O.I.P.E. PATENT DATE  
SCANNED TM<sup>3</sup> Q.A. *WLB*

APPLICATION NO. 09/667559	CONT/PRIOR F	CLASS 257	SUBCLASS 500+	ART UNIT <del>2811</del> 5814	EXAMINER FARAHANI <del>CHANDHOK</del>
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## APPLICANTS

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Power MOS transistor having trench gate

## ЗНАЧЕНИЕ

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
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<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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